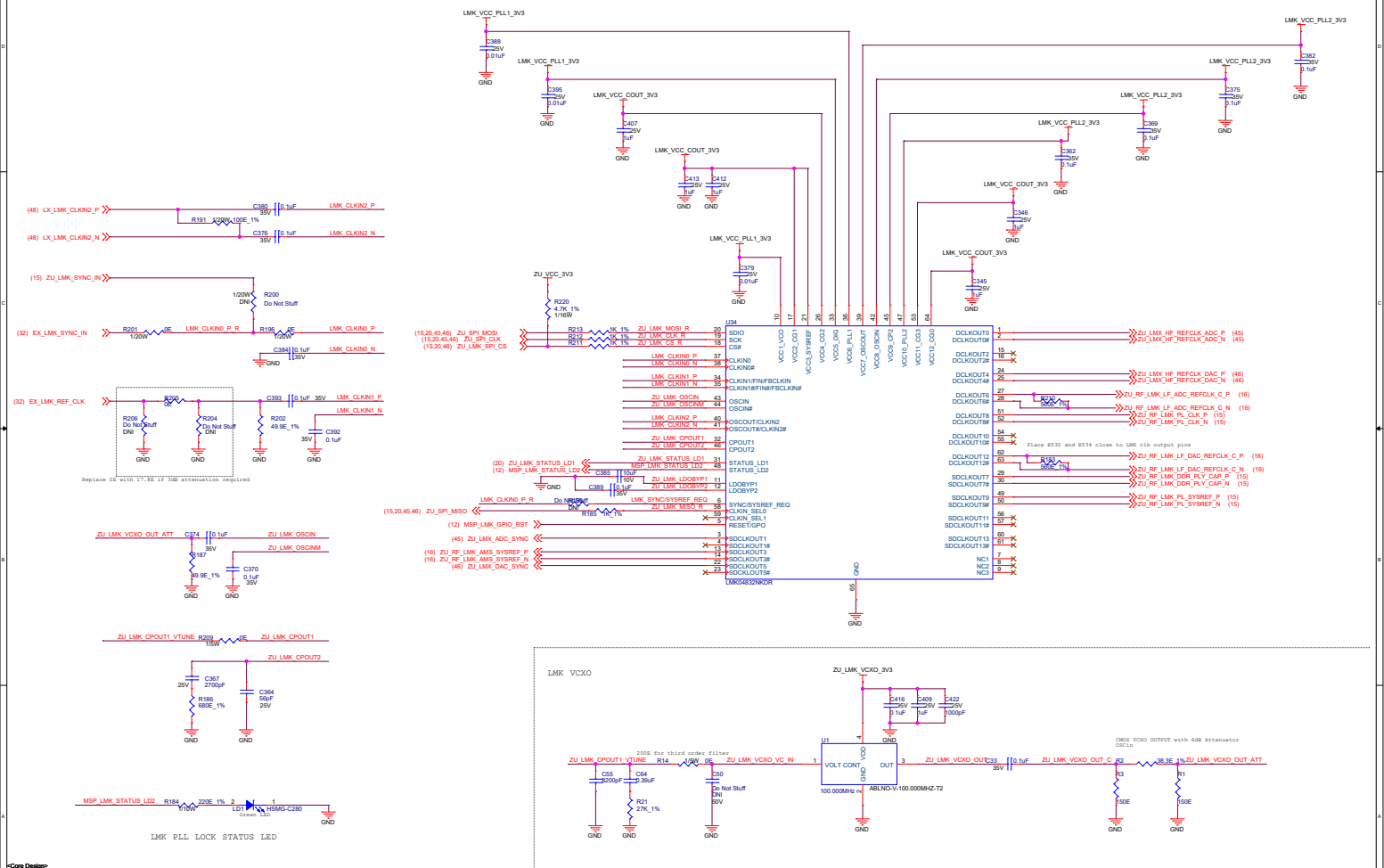
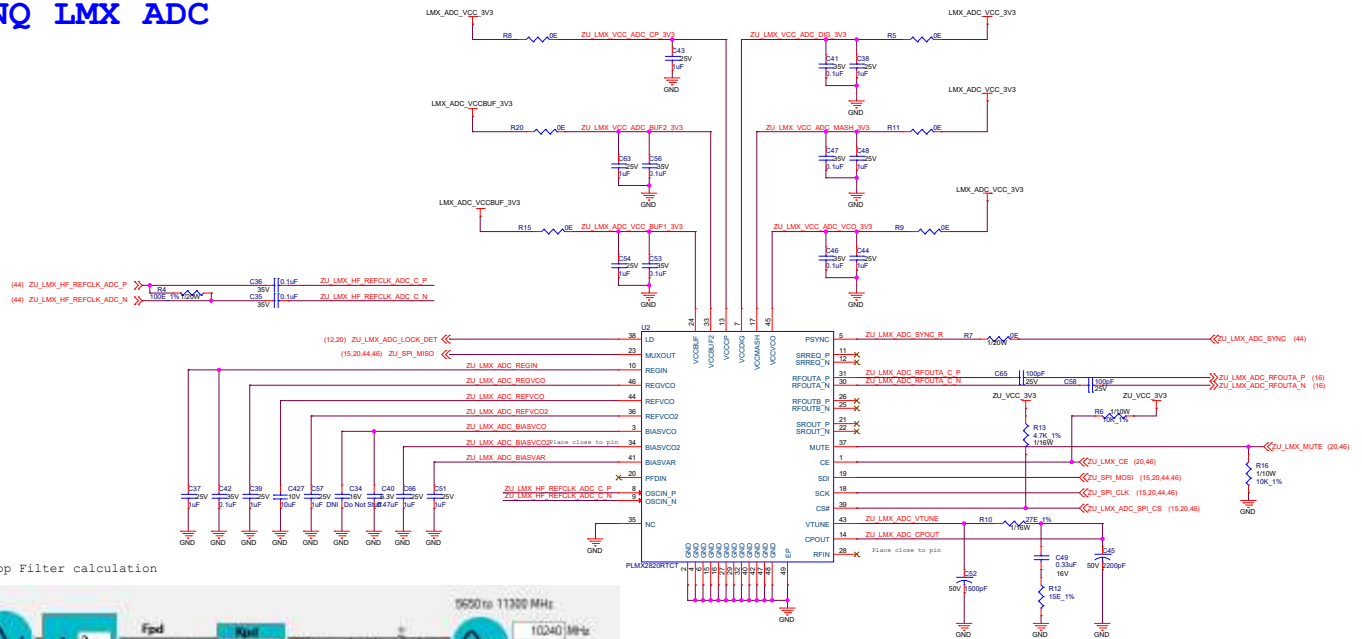


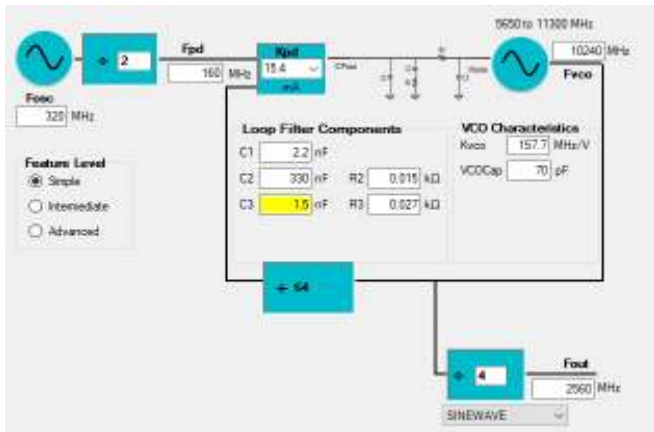
ZYNQ LMK SLAVE CLOCK GENERATOR

[illegible]

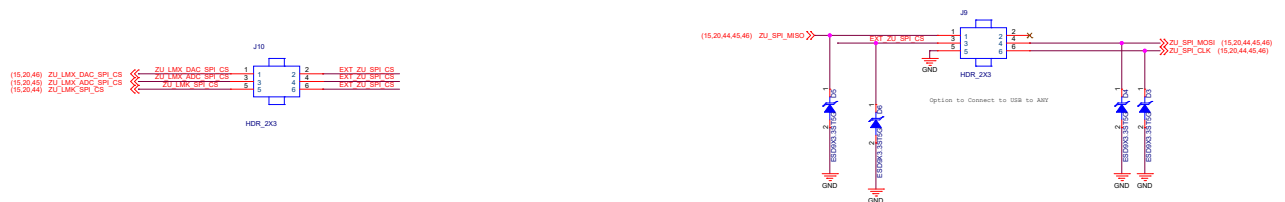
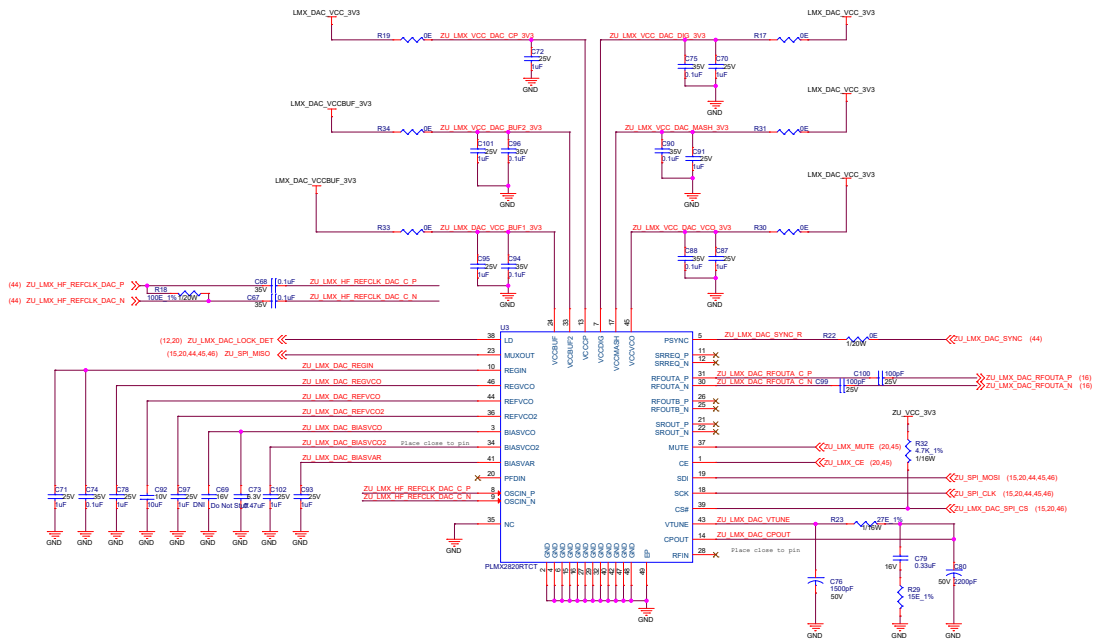
ZYNQ LMX ADC



Loop Filter calculation

[illegible]

ZYNQ LMX DAC



<Core Design>						
REV NO.	NATURE OF CHANGE	APPROVED BY	DATE	Mistral Solutions [P] Ltd. #60 Adarsh regent,100 Feet Ring Road, Dombivli Extension Bangalore: 560 071, Ph : +91-80-30912600, Fax :+91-80-2535 6444 Drawn by: Kshan Kumar Approved by: <Appet by> Date: Date Thursday, September 01, 2022 Design file path:	Title ZYNQ_CLOCK_DAC_LMX	
				Size C	Document Number B	Rev
				Date: Thursday, September 01, 2022		Sheet 46 of 52